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Day: Friday Date: 4/6/2007 Time: 12:51:02

Inventor Name Search Result

Your Search was:

Last Name = GREGORIUS

First Name = PETER

Application#	Patent#	Status	Date Filed	Title	Inventor Name	
10090318	6710603	150	03/04/2002	OVERLOAD PROTECTION CIRCUIT FOR LINE DRIVERS	GREGORIUS, PETER	
10131375	6700361	150	04/24/2002	VOLTAGE REGULATOR WITH A STABILIZATION CIRCUIT FOR GUARANTEEING STABILE OPERATION.	GREGORIUS, PETER	
10219275	7173993	150	08/16/2002	METHOD FOR SAMPLING PHASE CONTROL	GREGORIUS, PETER	
10225637	7095803	150	08/22/2002	METHOD OF RECONSTRUCTING DATA TRANSMITTED OVER A TRANSMISSION PATH IN A RECEIVER AND CORRESPONDING DEVICE	GREGORIUS, PETER	
10229867	7106812	150	08/28/2002	DEVICE FOR THE RECOVERY OF DATA FROM A RECEIVED DATA SIGNAL	GREGORIUS, PETER	
10301444	7184502	150	11/21/2002	CIRCUIT ARRANGEMENT FOR RECOVERING CLOCK AND DATA FROM A RECEIVED SIGNAL	GREGORIUS, PETER	
10407033	Not Issued	61		Method and apparatus for phase detection	GREGORIUS, PETER	
10478452	7209004	150	06/25/2004		GREGORIUS, PETER	
10481856	Not Issued	41		Transmitter for transmitting digital data over a transmission line	GREGORIUS, PETER	
10485336	Not Issued	95	I .	LINE DRIVER FOR TRANSMITTING DATA	GREGORIUS, PETER	
10492390	7088976	150	10/18/2004	DEVICE FOR	GREGORIUS,	

				RECONSTRUCTING DATA FROM A RECEIVED DATA SIGNAL AND CORRESPONDING TRANSCEIVER	PETER
10714202	Not Issued	30	11/14/2003	Method for reconstructing data clocked at a symbol rate from a distorted analog signal	GREGORIUS, PETER
10714811	Not Issued	41	11/17/2003	Method and filter arrangement for digital recursive filtering in the time domain	GREGORIUS, PETER
10737728	6911930	150	12/15/2003	CELL ARRAY WITH MISMATCH REDUCTION	GREGORIUS, PETER
10755696	7127061	150	01/12/2004	LINE DRIVER FOR DIGITAL SIGNAL TRANSMISSION	GREGORIUS, PETER
10808143	7154809	150	03/24/2004	METHOD FOR MEASURING THE DELAY TIME OF A SIGNAL LINE	GREGORIUS, PETER
<u>10808167</u>	Not Issued	30	03/24/2004	Feed forward equalizer and a method for analog equalization of a data signal	GREGORIUS, PETER
10809122	Not Issued	30	03/25/2004	Clock and data recovery unit	GREGORIUS, PETER
10810981	Not Issued	30	03/26/2004	Feed forward clock and data recovery unit	GREGORIUS, PETER
10916776	Not Issued	30	08/12/2004	Device for the production of standard-compliant signals	GREGORIUS, PETER
10954869	7180821	150		MEMORY DEVICE, MEMORY CONTROLLER AND MEMORY SYSTEM HAVING BIDIRECTIONAL CLOCK LINES	GREGORIUS, PETER
10955177	7173877	150	09/30/2004	MEMORY SYSTEM WITH TWO CLOCK LINES AND A MEMORY DEVICE	GREGORIUS, PETER
10970516	Not Issued	30	10/21/2004	Method and device for estimating channel properties of a transmission channel	GREGORIUS, PETER
10986767	7050340	150	11/15/2004		GREGORIUS, PETER
10996956	Not	41	11/24/2004	Integrated memory device and	GREGORIUS,

	Issued			memory module	PETER
11002148	Not Issued	30	12/03/2004	Semiconductor memory module	GREGORIUS, PETER
<u>11046160</u>	Not Issued	95	01/28/2005	MEMORY DEVICE HAVING COMPONENTS FOR TRANSMITTING AND RECEIVING SIGNALS SYNCHRONOUSLY	GREGORIUS, PETER
11128789	Not Issued	41	05/13/2005	Memory system and method of accessing memory chips of a memory system	GREGORIUS, PETER
11152769	7184360	150	06/15/2005	HIGH-SPEED INTERFACE CIRCUIT FOR SEMICONDUCTOR MEMORY CHIPS AND MEMORY SYSTEM INCLUDING SEMICONDUCTOR MEMORY CHIPS	GREGORIUS, PETER
11170887	Not Issued	30	06/30/2005	Synchronous signal generator	GREGORIUS, PETER
11171110	Not Issued	30	06/30/2005	Semiconductor memory system	GREGORIUS, PETER
11171585	Not Issued	30	06/30/2005	Semiconductor memory chip and method of protecting a memory core thereof	GREGORIUS, PETER
11174950	Not Issued	30		Memory controller with a plurality of parallel transfer blocks	GREGORIUS, PETER
11192335	Not Issued	41	07/29/2005		GREGORIUS, PETER
11193184	Not Issued	30	07/29/2005	Semiconductor memory chip and memory system	GREGORIUS, PETER
11194509	Not Issued	30	08/01/2005	Device for setting a clock delay	GREGORIUS, PETER
11194510	Not Issued	30			GREGORIUS, PETER
11194770	Not Issued	71		Method and device for generating a clock signal	GREGORIUS, PETER
11194773	Not Issued	30		Method of operating a memory device, memory module, and a memory device comprising the memory module	GREGORIUS, PETER
11213550	Not Issued	30		Code driver for a memory controller	GREGORIUS, PETER
<u>11226456</u>	Not	41	09/15/2005	Semiconductor memory chip with	GREGORIUS,

	Issued			re-drive unit for electrical signals	PETER
11226457	Not Issued	30	09/15/2005	High-speed interface circuit for semiconductor memory chips and memory system including the same	GREGORIUS, PETER
11238331	Not Issued	30	09/26/2005	Phase locked loop having reduced inherent noise	GREGORIUS, PETER
11239829	Not Issued	30		Semiconductor memory system and memory module	GREGORIUS, PETER
11242150	Not Issued	93	10/04/2005	SEMICONDUCTOR MEMORY CHIP	GREGORIUS, PETER
11259376	Not Issued	30	10/26/2005	Method of transferring signals between a memory device and a memory controller	GREGORIUS, PETER
11261911	Not Issued	30		Semiconductor memory chip, semiconductor memory module and method for transmitting write data to semiconductor memory chips	GREGORIUS, PETER
11264059	Not Issued	30	:	Memory module and memory device and method of operating a memory device	GREGORIUS, PETER
11264060	Not Issued	30		Generating a sampling clock signal in a communication block of a memory device	GREGORIUS, PETER
11267930	Not Issued	30	11/04/2005	Timing recovery phase locked loop	GREGORIUS, PETER

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Application#	Patent#	Status	Date Filed	Title	Inventor Name		
11331478	Not Issued	93	01/13/2006	SYNCHRONOUS PARALLEL/SERIAL CONVERTER	GREGORIUS, PETER		
11343946	Not Issued	25	01/30/2006	Method of transmitting data between different clock domains	GREGORIUS, PETER		
11345668	Not Issued	30		Synchronization and data recovery device	GREGORIUS, PETER		
11346993	Not Issued	30	02/03/2006	Data handover unit for transferring data between different clock domains	GREGORIUS, PETER		
11355801	Not Issued	90	02/16/2006	CONTROL UNIT FOR DEACTIVATING AND ACTIVATING THE CONTROL SIGNALS	GREGORIUS, PETER		
11357413	Not Issued	30	02/21/2006	Memory module device	GREGORIUS, PETER		
11509092	Not Issued	25	08/24/2006	Semiconductor memory system and semiconductor memory chip	GREGORIUS, PETER		
11554554	Not Issued	20	10/30/2006	APPARATUS AND METHOD FOR AVOIDING STEADY- STATE OSCILLATIONS IN THE GENERATION OF CLOCK SIGNALS	GREGORIUS, PETER		
11590488	Not Issued	30	10/30/2006	Data transfer in a memory device	GREGORIUS, PETER		
11679732	Not Issued	19	02/27/2007	MEMORY ARRANGEMENT	GREGORIUS, PETER		
11686818	Not Issued	19			GREGORIUS, PETER		
60457634	Not Issued	159		Methodik zur dynamischen laufzeitermittlung von bi-	GREGORIUS, PETER		

				direktionalen datenbussen	<u> </u>
60457690	Not Issued	159	!		GREGORIUS, PETER
60457811	Not Issued	159	03/26/2003	Programmierbare vorverstarker mit einfacher entzerrung	GREGORIUS, PETER
60457812	Not Issued	159			GREGORIUS, PETER

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